

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination MUI ET AL.	
		Examiner Eric B. Chen	Art Unit 1765	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2005/0085090	04-2005	Mui et al.	438/714
	B	US-6,620,631	09-2003	Tao et al.	438/9
	C	US-5,858,847	01-1999	Zhou et al.	438/305
	D	US-6,245,581	06-2001	Bonser et al.	438/8
	E	US-6,479,200	11-2002	Stirton, James Broc	430/30
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Streetman, Solid State Electronic Devices, 1990, Prentice Hall, 3rd ed., p. 300-301.
	V	Wolf et al., Silicon Processing for the VLSI Era, 1986, Lattice Press, Vol. 1, pp. 384-85.
	W	Wolf, Silicon Processing for the VLSI Era, 2002, Lattice Press, Vol. 4, pp. 145-46.
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.